

## AMENDMENTS

This section presents changes to the specification and the claims in a clean-unmarked format. A version with markings to show the changes made by the current amendment is provided after the remarks section.

### In The Title:

Please replace the current title with the new title:

-- Testing a Bus Using Bus Specific Instructions --

### In The Specification:

Please replace the paragraph beginning at page 8, line 10 with the paragraph:

H1  
A similar problem occurs in the development of components such as chipsets and processors. In the early design stages, the components being developed are simulated in a software environment and are often driven by a high-level bus model known in the art as a bus functional model (BFM). A simulation stimulus software language may then be used to exercise the simulated system. While these methods provide a designer a high level of control, observability and repeatability when testing system designs, the simulation environments are extremely slow, typically operating at about 1-10 Hz. Further, there is no corresponding capability for repeating the simulation on actual systems (hardware) during the design stages that provides the desired control, observability, and repeatability.--

Please replace the paragraph beginning at page 15, line 14 with the paragraph:

H2  
--The host computer 80 provides a programming and control interface to the transaction generator 20. In addition to providing the binary object file 84 containing the